TRAINING PROGRAM

VLSI

Very-large-scale integration (VLSI) is the process of creating an integrated circuit (IC) by combining hundreds of thousands of transistors or devices into a single chip. VLSI began in the 1970s when complex semiconductor and communication technologies were being developed.

KEY FEATURES

Effective Upskilling Planned Curriculum
Team Learning Awesome Quizzes
Complete Hands on

The below Curriculum is Schedule for 2 weeks

CURRICULUM

Introduction to VLSI overview
VLSI design flow
abstract explanation and title finalization.

Introduction to digital electronics
Introduction to digital logic design: combinational logic

Advanced digital electronics
Introduction to digital logic design: Sequential logic
Introduction to digital logic design: Sequential logic
advanced digital electronics(sequential circuits)

FSM, Memory, FIFO

Introduction to Verilog HDL for design: different abstraction
levels, module syntax, lexical conventions
introduction to Verilog HDL for design: data types, operators,

Introduction to Xilinx ISE 14.7i platform
Introduction to Verilog HDL for Verification:
Introduction to testbenches

introduction to verilog HDL for design:
combinational
sequential logic models

Introduction to Digital Logic Design: Finite State Machines, Memories

Implementation on Xilinx ISE 14.7
Implementation on Xilinx ISE 14.7
Implementation on Xilinx ISE 14.7

introduction to CMOS design
Implementation using CMOS

Introduction to Microwind and DSCH
implementation using Microwind and DSCH

implementation using Microwind and DSCH

SAK INFORMATICS
Corporate office: #401, Venkata Satyadeva Enclave, Balaji Colony, Nizampet-90